

We claim:

1. A device for minimizing interblock interference, wherein the device is adapted to compute an optimum time of reference based on minimizing total interblock interference power.
2. The device of claim 1, wherein computing an optimum time of reference comprises generating a windowing function which reflects the fact that points along tails of an impulse response contribute non-uniform amounts of interblock interference.
3. The device of claim 2, wherein computing an optimum time of reference further comprises generating a time of reference-optimizing function by minimizing a cross-correlation between the windowing function and a square of the impulse response.
4. The device of claim 3, wherein computing an optimum time of reference further comprises:

computing an output value of the time of reference-optimizing function;

and

identifying the optimum time of reference as a location of the output value.
5. The device of claim 1, wherein the device comprises a transceiver module.

6. The device of claim 1, wherein the device comprises a DMT transceiver.

7. A method for minimizing interblock interference comprising:

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computing an optimum time of reference based on minimizing total
interblock interference power.

8. The method of claim 7, wherein computing an optimum time of reference
10 comprises generating a windowing function which reflects that points
along tails of an impulse response contribute non-uniform amounts of
interblock interference.

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9. The method of claim 8, wherein computing an optimum time of reference
15 further comprises generating a time of reference-optimizing function by
minimizing a cross-correlation between the windowing function and a
square of the impulse response.

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10. The method of claim 9, wherein computing an optimum time of reference
20 further comprises:

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computing an output value of the time of reference-optimizing function;
and

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identifying the optimum time of reference as a location of the output value.